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Communications and Electronics Engineer (1992) with a Master in Science on Computer Engineering (1994 ) from the National Polytechnic Institute. In 1999 he received his PhD of Science from the Polytechnic University of Cataluña, Spain . From 1999-2002 he held a postdoctoral fellowship at MIT working on the SCALE project. He is currently responsible director of CIC , areas of interest are: Microarchitecture High Performance Computers , Computer Microarchitecture low power consumption, VLSI design .